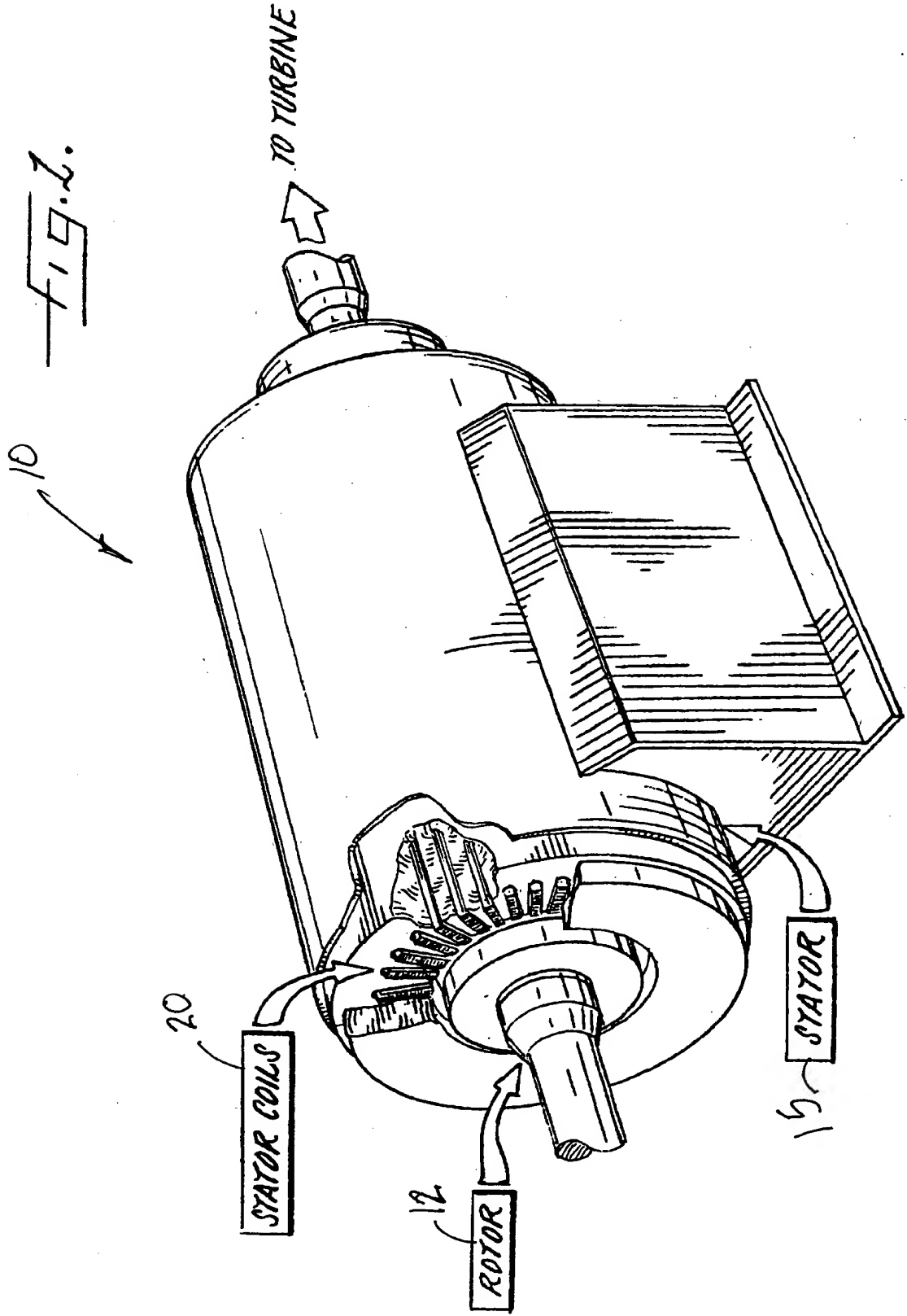


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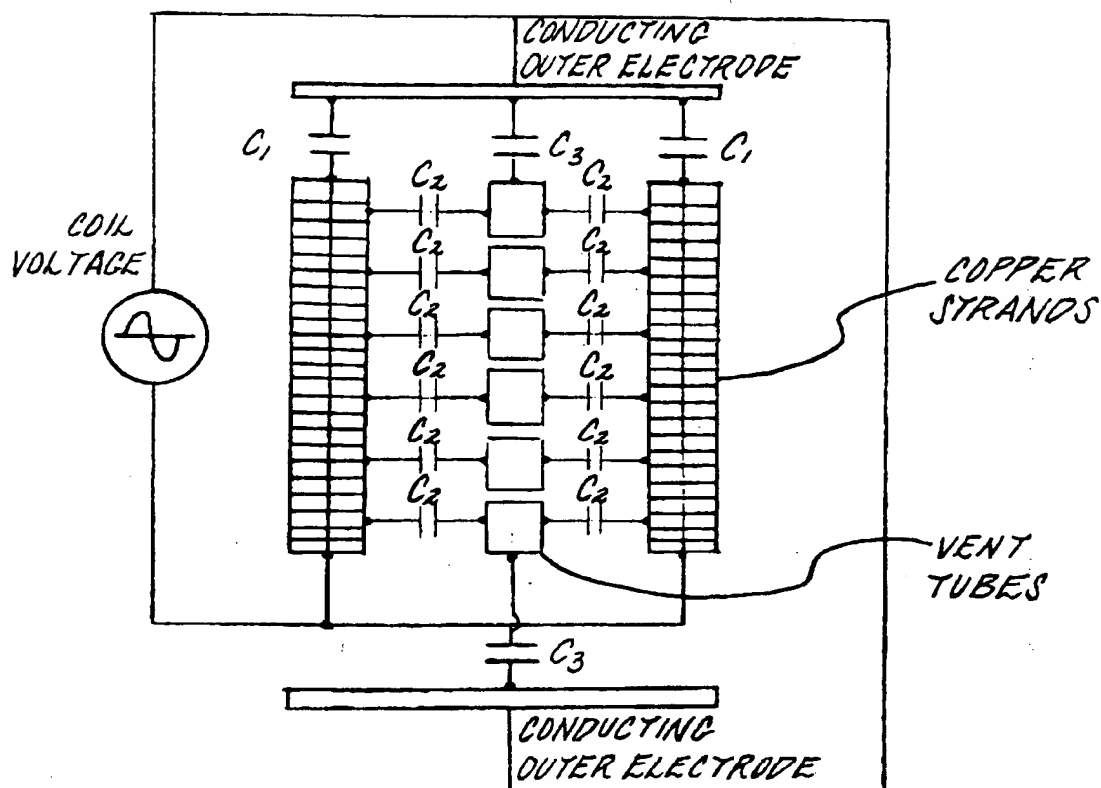


A blank coordinate plane with x and y axes. The x-axis is horizontal and the y-axis is vertical, intersecting at the origin. There are no tick marks or labels on the axes.

FIG. 3 and FIG. 4 are isometric views of a multi-layered assembly. FIG. 3 shows a stack of layers (30) with a central core (40) and a roll of material (45) being applied to the side. FIG. 4 shows a similar assembly with a different internal structure (41) and a roll of material (45) being applied to the side. Both figures include reference numerals 20, 30, 40, 41, 42, 44, and 45.

Fig. 4.





$C_1$  - CAPACITIVE COUPLING BETWEEN OUTER ELECTRODE TO COPPER STRANDS

$C_2$  - CAPACITIVE COUPLING BETWEEN COPPER STRANDS AND ALL COOLING TUBES

$C_3$  - CAPACITIVE COUPLING BETWEEN OUTER ELECTRODE AND TOP AND BOTTOM TUBES (TOP SURFACE OF TUBES ONLY)

— FIG. 7.

OUTER GROUND ELECTRODE

$V_1$

COIL VOLTAGE

$C_1$

$C_3$

$C_2$

$R$

TUBES

$V_{C_2}$

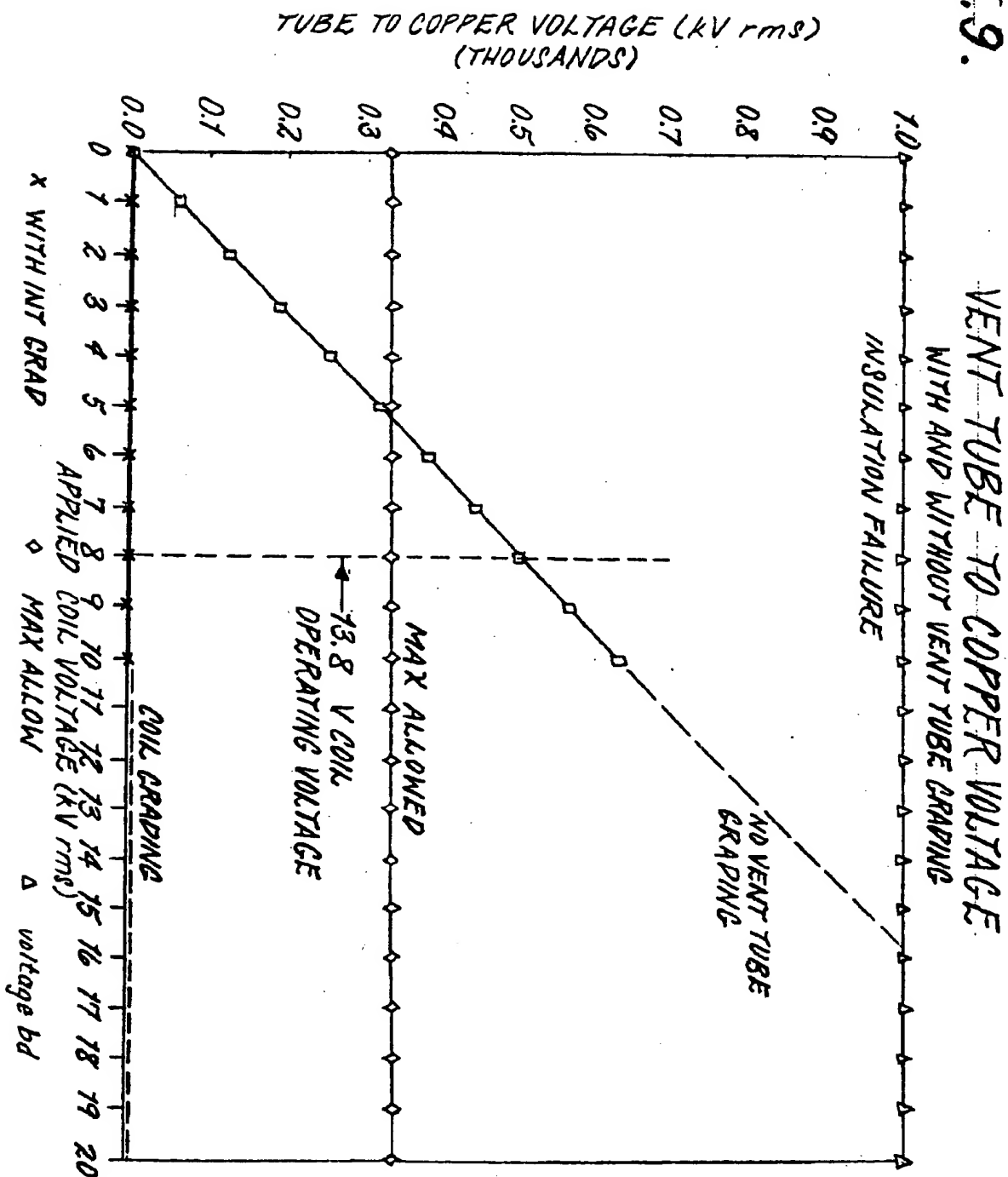
$C_1, C_2, C_3$   
DEFINED IN  
FIGURE 7

$$V_{C2} = \frac{X_{C2}}{X_{C3} + X_{C2}} \cdot V_1 \quad X = \text{CAPACITOR REACTANCE}$$

$R \equiv$  VOLTAGE GRADING RESISTOR

Fig. 8.

Fig. 9.



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